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	Sui		~ · · · ·		Application Number	N/A		
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			IT BY APP		First Named Inventor	Sander L. Gierkink		
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T	Sheet	1	of	2	Attorney Docket Number	S. GIERKINK 2-2		

U. S. PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (F known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
CH		^{US-} 6,029,059	02/22/2000	Bojer			
1		^{US-} 2001/0021645A1	09/13/2001	Ugajin, et al.			
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FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ Number ⁴ 'Kind Code ⁵ (<i>lf known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁰		
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Translation is attached.

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5	Substitute fo	r form 1449B/PTO			Complete if Known		
					Application Number	N/A	
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STATEMENT BY APPLICANT					First Named Inventor	Sander L. Gierkink	
					Art Unit	N/A- 2685	
(Use as many sheets as necessary)					Examiner Name	NHA HANNON	
	Sheet	2	of	2	Attorney Docket Number	S. GIERKINK 2-2	

NON PATENT LITERATURE DOCUMENTS									
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposlum, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.							
CH		"2-GHz RF Front-End Circuits in CMOS/SIMOX Operating at an Extremely Low Voltage of 0.5 V" by Mitsuru Harada, et al.; Dec. 2000 IEEE; pp. 2000-2004							
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)	"A 2 GHz Merged CMOS LNA and Mixer for WCDMA" by Ali Karimi-Sanjaani, et al.; June 2001 Symposium on VLSI Circuits Digest of Technical Papers; pp. 19-22	(200 0 0 424 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4						
		"A 1V 12mW 2GHz Receiver with 49 dB Image Rejection in CMOS/SIMOX" by M. Ugajin, et al; Digest of technical Papers of 2001 Intl. Conf. on Solid State Circuits; pp. 288-289; Feb. 2001							
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